

SEP 15 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Theodore W. Houston

Docket No.: TI-35657

Serial No.: 10/667,615

Art Unit: 2811

Filed: 9/22/2003

Examiner: Owens, Douglas

Title: A Multi-Gate One-Transistor Dynamic Random Access Memory

AMENDMENT UNDER 37 CFR §1.116

September 15, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATION OF FACSIMILE TRANSMISSION
I hereby certify that the above correspondence is being
transmitted by facsimile to the U.S. Patent and Trademark Office
at 571.273.8300 on the date shown below.

Karen Vertz
Karen Vertz

9-15-05
Date

Dear Commissioner:

In response to the Office Action, dated 05/17/2005, in the above-identified patent application, please make the following amendments. They are respectfully submitted as a full and complete response to that Action. Please charge any required fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.